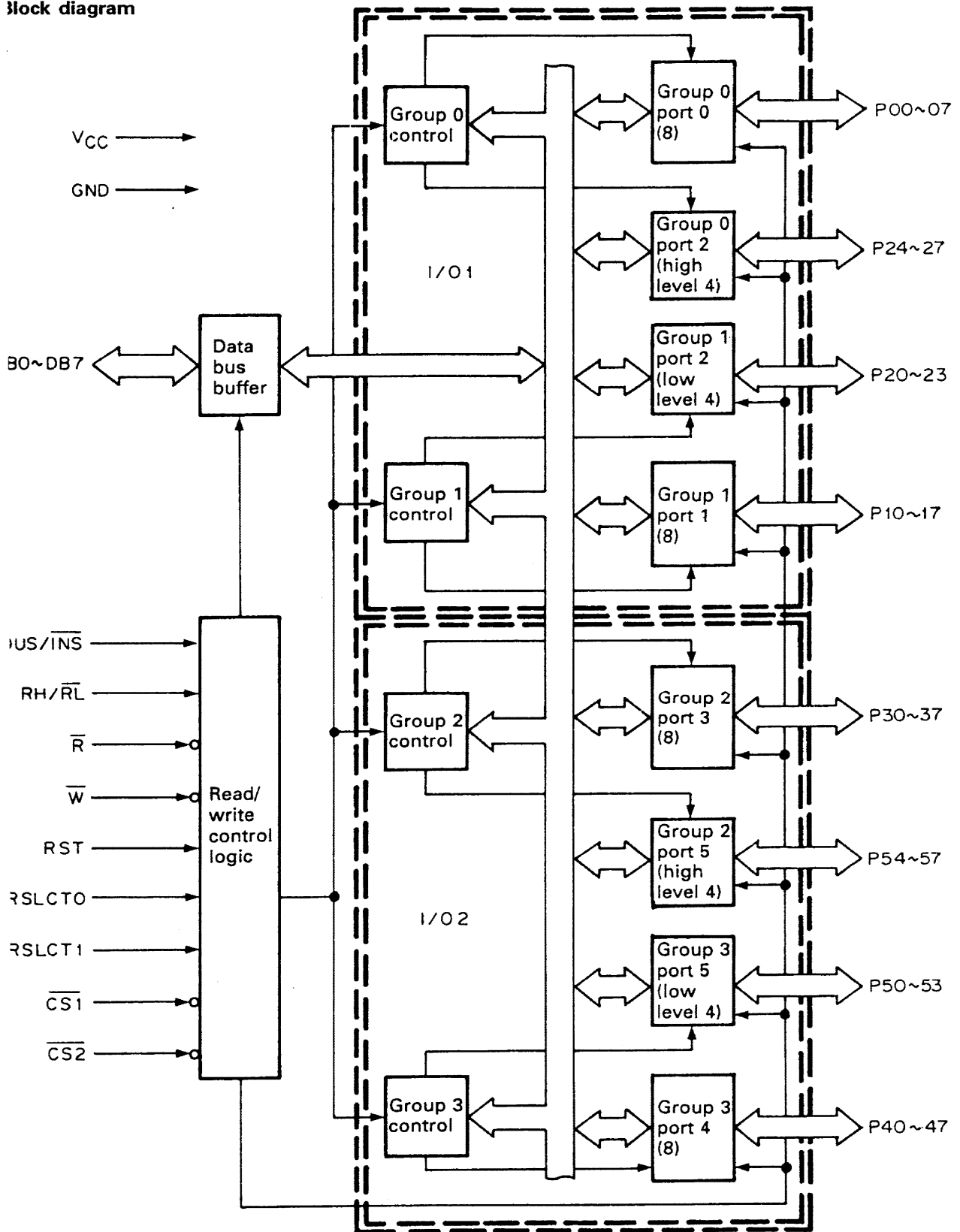# SEMICONDUCTOR DATA

Port : MB89363B

Block diagram

SEMICONDUCTOR DATA

• Terminal function

Pin No.	Pin name	Name	I/O	Function
1~4 77~80	P30~P37	Port 3 all bits	I/O	Eight-bit general-purpose input/output port. These terminals are included in group 2. Three operation modes can be selected by setting the control parameter by software.
5	$\overline{W}$	Write	I	The control parameter and port output data item can be written using a low-level signal. The parameter and port data can be distinguished and selected using the $\overline{CS1}$, $\overline{CS2}$, RSLCT0, and RSLCT1 signals.
6	RST	Initial setting reset	I	Input terminal. The M889363B is set to the initial mode using a reset signal, and initial value 9B (hexadecimal) is automatically set for two control parameters. The initial mode indicates that all ports are in the input state of mode 0. All port terminals stay high in the initial mode. The active signal level is selected using an RH/$\overline{RL}$ signal. RH/$\overline{RL}$ = 0 : $\overline{RST}$ (active low) RH/$\overline{RL}$ = 1 : RST (active high)
9	RH/$\overline{RL}$	Reset active level selection	I	The RST terminal is set to active high or active low. RH/$\overline{RL}$ = 0 : $\overline{RST}$ (active low) RH/$\overline{RL}$ = 1 : RST (active high) The RH/$\overline{RL}$ terminal is fixed at either Vcc ot GND at all times.
11	OUS/$\overline{INS}$	Port 0 and 3 read value selection	I	This terminal indicates the output state of ports 0 and 3. It also selects whether the external terminal value of ports 0 and 3 is read directly or whether the output latch value of ports 0 and 3 is read directly when reading the value of ports 0 and 3. OUS/$\overline{INS}$ = 0 : The output latch value of ports 0 and 3 is read. OUS/$\overline{INS}$ = 1 : The external terminal value of ports 0 and 3 is read.
12~19	DB0~DB7	Bidirectional data bus	I/O	Eight-bit, bidirectional data bus. These terminals are used for data communication with the MPU. The bus signal making and breaking and data direction are controlled using the $\overline{CS1}$, $\overline{CS2}$, $\overline{R}$ and $\overline{W}$ signals.
20~23 25~28	P00~P07	Port 0 all bits	I/O	Eight-bit, general-purpose input/output port. These terminals are included in group 0. Three operation modes can be selected by setting the control parameter by software.
29 75	$\overline{CS1}$ $\overline{CS2}$	Device selection	I	When a low-level signal is input to this terminal, signals DB0 through DB7 are released and data communication with the MPU takes place. At that time, the control parameter is written, and data is written into or read from each port. $\overline{CS1}$ = 0 : I/O1 $\overline{CS2}$ = 0 : I/O2 Simultaneous selection of $\overline{CS1}$ = 0 and $\overline{CS2}$ = 0 is inhibited.
30, 74	GND	Ground terminal	I	0V.
31 32	RSLCT0 RSLCT1	Access selection	I	When data is sent to the MPU, the parameter and port are distinguished and selected using the $\overline{CS1}$, $\overline{CS2}$, RSLCT0, and RSLCT1 signals.
34~40 43	P20~P27	Port 2 all bits	I/O	These terminals are used as a general-purpose input/output port, handshaking control terminals, and status data bit input/output terminals in accordance with the operation functions and modes of groups 0 and 1.
44~51	P10~P17	Port 1 all bits	I/O	Eight-bit, general-purpose input/output port. These terminals are included in group 1. Two operation modes can be selected by setting the control parameter by software.
53	Vcc			+5V power.
54~61	P40~P47	Port 4 all bits	I/O	Eight-bit, general-purpose input/output port. These terminals are included in group 3. Two operation modes can be selected by setting the control parameter by software.
62 65~71	P50~P57	Port 5 all bits	I/O	These terminals are used as a general-purpose input/output port, handshaking control terminals, and status data bit input/output terminals.
76	$\overline{R}$	Read	I	Data from each port is read using a low-level signal. The port type is selected using the $\overline{CS1}$, $\overline{CS2}$, RSLCT0, and RSLCT1 signals.
7,8,10,24 33,41,42 52,63,64 72,73	NC	–	–	Connection to the NC terminal is inhibited.